

09/30/03

Form PTO-1449			ATTORNEY DOCKET NO. AUS920030483US1		SERIAL NO. Not Assigned 10/675, 721
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT Levine et al.		
			FILING DATE	GROUP ART UNIT Not Assigned 2191	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
TTV	AA 5,103,394	Apr. 7, 1992	Blasciak	<del>395/575</del>	Dec. 21, 1989
TTV	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	<del>712/235</del>	Feb. 23, 1999
TTV	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	<del>711/425</del>	Dec. 15, 2000
TTV	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	<del>717/154</del>	Feb. 26, 1999
TTV	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	<del>717/4</del>	May 4, 1998
TTV	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	<del>395/706</del>	Jun. 9, 1997
TTV	AG 6,351,844 B1	Feb. 26, 2002	Bala	<del>717/4</del>	Nov. 5, 1998
TTV	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	<del>717/8</del>	Sep. 30, 1998
TTV	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	<del>709/108</del>	Nov. 26, 1997
TTV	AJ 5,774,724	Jun. 30, 1998	Heisch	<del>395/704</del>	Nov. 20, 1995
TTV	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	<del>395/704</del>	Aug. 21, 1997
TTV	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	<del>717/5</del>	Nov. 2, 1998
TTV	AM 5,691,920	Nov. 25, 1997	Levine et al.	<del>364/551.01</del>	Oct. 2, 1995
TTV	AN 6,223,338 B1	Apr. 24, 2001	Smolders	<del>717/4</del>	Sep. 30, 1998
TTV	AO 6,101,524	Aug. 8, 2000	Choi et al.	<del>709/102</del>	Oct. 23, 1997
TTV	AP 6,256,775 B1	Jul. 3, 2001	Flynn	<del>717/4</del>	Dec. 11, 1997
TTV	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.	<del>702/186</del>	Jun. 30, 1999
TTV	AR 6,134,676	Oct. 17, 2000	VanHuben et al.	<del>714/39</del>	Apr. 30, 1998
TTV	AS 5,937,437	Aug. 10, 1999	Roth et al.	<del>711/202</del>	Oct. 28, 1996
TTV	AT 6,243,804 B1	Jun. 5, 2001	Cheng	<del>712/228</del>	Jul. 22, 1998
TTV	AU 4,291,371	Sep. 22, 1981	Holtey	<del>364/200</del>	Jan. 2, 1979
TTV	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.	<del>714/45</del>	Nov. 10, 1997
TTV	AW 6,286,132 B1	Sep. 4, 2001	Tanaka et al.	<del>717/4</del>	Jan. 7, 1999
TTV	AX 6,206,584 B1	Mar. 27, 2001	Hastings	<del>395/183.11</del>	May 31, 1995
TTV	AY 6,374,364 B1	Apr. 16, 2002	McElroy et al.	<del>714/10</del>	Jan. 19, 1999
TTV	AZ 6,070,009	May 30, 2000	Dean et al.	<del>395/704</del>	Nov. 26, 1997
TTV	BA 5,966,537	Oct. 12, 1999	Ravichandran	<del>395/709</del>	May 28, 1997
DATE CONSIDERED 2/16/07			EXAMINER TED T. VO		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					

<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. <b>AUS920030483US1</b>		SERIAL NO. <b>Not Assigned</b>  <div style="font-size: 1.5em; font-family: cursive;">10/675,721</div>	
		APPLICANT <b>Levine et al.</b>			
		FILING DATE		GROUP ART UNIT <b>Not Assigned</b>	

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
TTU	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
TTU	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
TTU	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
TTU	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
TTU	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
TTU	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001

FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
TTU	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
TTU	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)		
TTU	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.
TTU	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.
TTU	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.
TTU	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.
TTU	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 <sup>th</sup> , Phoenix, Feb. 20-22, 2000, pp. 516-524.
TTU	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.

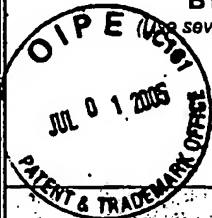
RELATED PATENT APPLICATIONS				
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
TTU	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
TTU	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED <b>2/16/07</b>	EXAMINER <b>TED T. VO</b>
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Form PTO-1449		ATTORNEY DOCKET NO. AUS920030483US1		SERIAL NO. 10/675,721	
<b>LIST OF PRIOR ART CITED BY APPLICANT</b> <small>(Use several sheets if necessary)</small>		APPLICANT Levine et al.			
		FILING DATE September 30, 2003		GROUP ART UNIT 2191	
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL		APPLICATION NO / ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
TTL	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
TTL	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
TTL	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
TTL	AD	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
TTL	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
TTL	AF	10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
TTL	AG	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
TTL	AH	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
TTL	AI	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
TTL	AJ	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
DATE CONSIDERED		EXAMINER			
TTL		TED			
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## Complete if Known

Application Number 10/675,721  
Filing Date 09/30/2003  
First Named Inventor Levine et al.  
Examiner Name Chan, Eddie P.  
Art Unit 2191  
Attorney Docket No. AUS920030483US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TTU	AA 4,034,353	07-1977	Denny et al.	
TTU	AB 4,821,178	04-1989	Levin et al.	
TTU	AC 5,151,981	05-1992	Westcott et al.	
TTU	AD 5,581,482	12-1996	Wiedenman et al.	
TTU	AE 5,594,864	01-1997	Trauben	
TTU	AF 5,691,920	11-1997	Levine et al.	
TTU	AG 5,758,168	05-1998	Mealey et al.	
TTU	AH 6,480,966	11-2002	Rawson, III	
TTU	AI 2003/0135720	07-2003	DeWitt et al.	
TTU	AJ 5,926,640	07-1999	Mason et al.	
TTU	AK 6,185,652	02-2001	Shek et al.	
TTU	AL 4,145,735	03-1979	Soga	
TTU	AM 5,548,762	08-1996	Creedon et al.	
TTU	AN 4,794,472	12-1988	Doyama	
TTU	AO 6,161,187	12-2000	Mason et al.	
TTU	AP 3,707,725	12-1972	Dellheim	
TTU	AQ 6,408,386	06-2002	Hammond et al.	
TTU	AR 6,775,728	08-2004	Zimmer et al.	
TTU	AS 6,757,771	06-2004	Christie	
TTU	AT 6,148,321	11-2000	Hammond	
TTU	AU 6,275,893	08-2001	Bonola	
TTU	AV 4,825,359	04-1989	Ohkami et al.	

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TTU	AW Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 <sup>nd</sup> Ed., 1982, pp.434-443.
TTU	AX Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEEExplore, October 1996, pp. 1-2.

Examiner:

TED T. VO

Date Considered:

2/16/07

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INFORMATION  
STATEMENT

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

**Complete if Known**

Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	<del>Nahar Gamrun</del>
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	524	525	526	527	528
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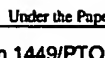
CAPITAL LETTERS) title of the ar

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TH/AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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2/16/07

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TTC	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
TTC	AI	TORRELLAS et al., "False Sharing and Spatial Locality In Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
TTC	AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

**Examiner:**

TED T. Va

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Examiner Name	Nehar Gaur
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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TR	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
TR	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
TR	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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Examiner Name	Nahar-Gemrun
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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FL	AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

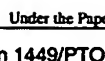
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nehor-German
Art Unit	2191
Attorney Docket No.	AUS920030483US1

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	<del>Nahar Gansun</del>
Art Unit	2191
Attorney Docket No.	AUS920030483US1

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Application Number	10/675,721
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Nahar Gantun
Art Unit	2191
Attorney Docket No.	AUS920030483US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TL	AA 5,857,097	01-05-1999	Henzinger et al.	
TL	AB 6,820,155	11-16-2004	Ito	
TL	AC 5,745,770	04-28-1998	Thangadurai et al.	

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TL AD	MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

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	Filing Date		2003-09-30	
	First Named Inventor		Levine et al.	
	Art Unit		2191	
	Examiner Name		<del>Nehar, Gannan</del>	
	Attorney Docket Number		AUS920030483US1	

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TTL	1	6944720	B2	2005-09-13	Sperber et al.			
TTL	2	7035996	B2	2006-04-25	Woodall et al.			
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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	<del>Levine et al.</del> LEVINE et al
Art Unit	2191
Examiner Name	<del>Nahar, Qamran</del>
Attorney Docket Number	AUS920030483US1

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear		
TTL	1	6973417	B1	2005-12-06	Maxwell et al.			
TTL	2	6721875	B1	2004-04-13	McCormick et al.			
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Application Number	10675721
Filing Date	2003-09-30
First Named Inventor	Levine et al.
Art Unit	2191
Examiner Name	Nehar, Camrun
Attorney Docket Number	AUS920030483US1

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	First Named Inventor	Levine et al.	
	Art Unit	2191	
	Examiner Name	Nahar, Qamran	
	Attorney Docket Number	AUS920030483US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	
TTL	1	5966538		1999-10-12	Granston et al.		
TTL	2	5940618		1999-08-17	Blandy et al.		
TTL	3	6189142	B1	2001-02-13	Johnston et al.		
TTL	4	5768500		1998-06-16	Agrawal et al.		
TTL	5	6026235		2000-02-15	Shaughnessy		
TTL	6	5950003		1999-09-07	Kaneshiro et al.		
TTL	7	6594820	B1	2003-07-15	Ungar		
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Application Number	10675721
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Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

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TTU	1	20020073406	A1	2002-06-13	Gove	
TTU	2	20030066055	A1	2003-04-03	Spivey	
TTU	3	20030131343	A1	2002-07-10	French et al.	
TTU	4	20040268316	A1	2004-12-30	Fisher et al.	

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Art Unit	2191
Examiner Name	Nahar, Qamrun
Attorney Docket Number	AUS920030483US1

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TTU	1	5761103		1998-06-02	Oakland et al.		
TTU	2	6549959	B1	2003-04-13	Yates et al.		
TTU	3	6772322	B1	2004-08-03	Merchant et al.		
TTU	4	5937437		1999-08-10	Roth et al.		
TTU	5	6070009		2000-05-30	Dean et al.		
TTU	6	6381679	B1	2002-04-30	Matsubara et al.		
TTU	7	6687794	B2	2004-02-03	Malik		
TTU	8	6539458	B2	2003-03-25	Holmberg		

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FTU	9	6973542	B1	2005-12-06	Schmuck et al.	
FTU	10	6782454	B1	2004-08-24	Damron	
FTU	11	5652858		1997-06-29	Okada et al.	
FTU	12	6848029	B2	2005-01-25	Coldewey	
FTU	13	5822790		1998-10-13	Mehrotra	

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TTL	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
TTL	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	
TTL	1	5657253		1997-08-12	Dreyer et al.		
TTL	2	5991708		1999-11-23	Levine et al.		
TTL	3	5564015		1996-10-08	Bunnell		
TTL	4	5450349		1995-09-12	Brown et al.		
TTL	5	5987598		1999-11-16	Levine et al.		
TTL	6	6009514		1999-12-18	Henzinger et al.		
TTL	7	6112317		2000-08-29	Berc et al.		
TTL	8	5896538		1999-04-20	Blandy et al.		

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TTU	9	5355487		1994-10-11	Keller et al.	
TTU	10	6948032	B2	2005-09-20	Kadambi et al.	

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TTU	1	"CPU Cache", Wikipedia, pp. 1-14 retrieved 11/1/06 <a href="http://en.wikipedia.org/wiki/CPU_cache">http://en.wikipedia.org/wiki/CPU_cache</a>	<input type="checkbox"/>
TTU	2	AMMONS et al., "Exploiting Hardware Performance Counters with Flow and Context Sensitive Profiling", 1997, ACM, pp. 85-96.	<input type="checkbox"/>



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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10675721
	Filing Date		2003-09-30
	First Named Inventor	Levine et al.	
	Art Unit	2191	
	Examiner Name	Nahar, Qamrun	
	Attorney Docket Number	AUS920030483US1	

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TTU	2	6928521	B1	2005-08-09	Burton et al.		
TTU	3	6598153	B1	2003-07-22	Flachs et al.		
TTU	4	5875294		1999-02-23	Roth et al.		
TTU	5	6073109		2000-06-06	Flores et al.		
TTU	6	6871298	B1	2005-03-22	Cavanaugh et al.		
TTU	7	5555432		1996-09-10	Hinton et al.		
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TTU	9	6865666	B2	2005-03-08	Yoshida et al.	
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